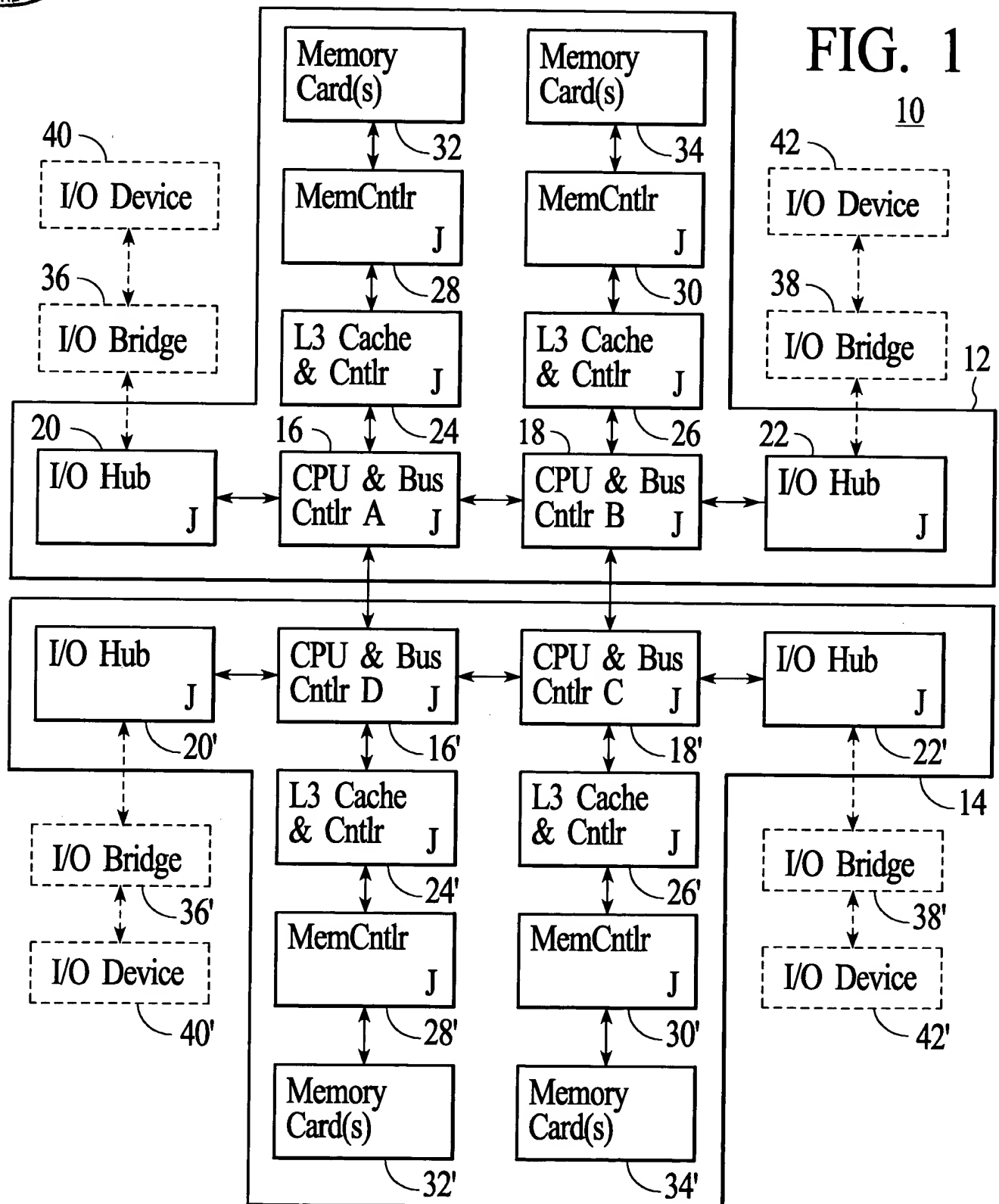


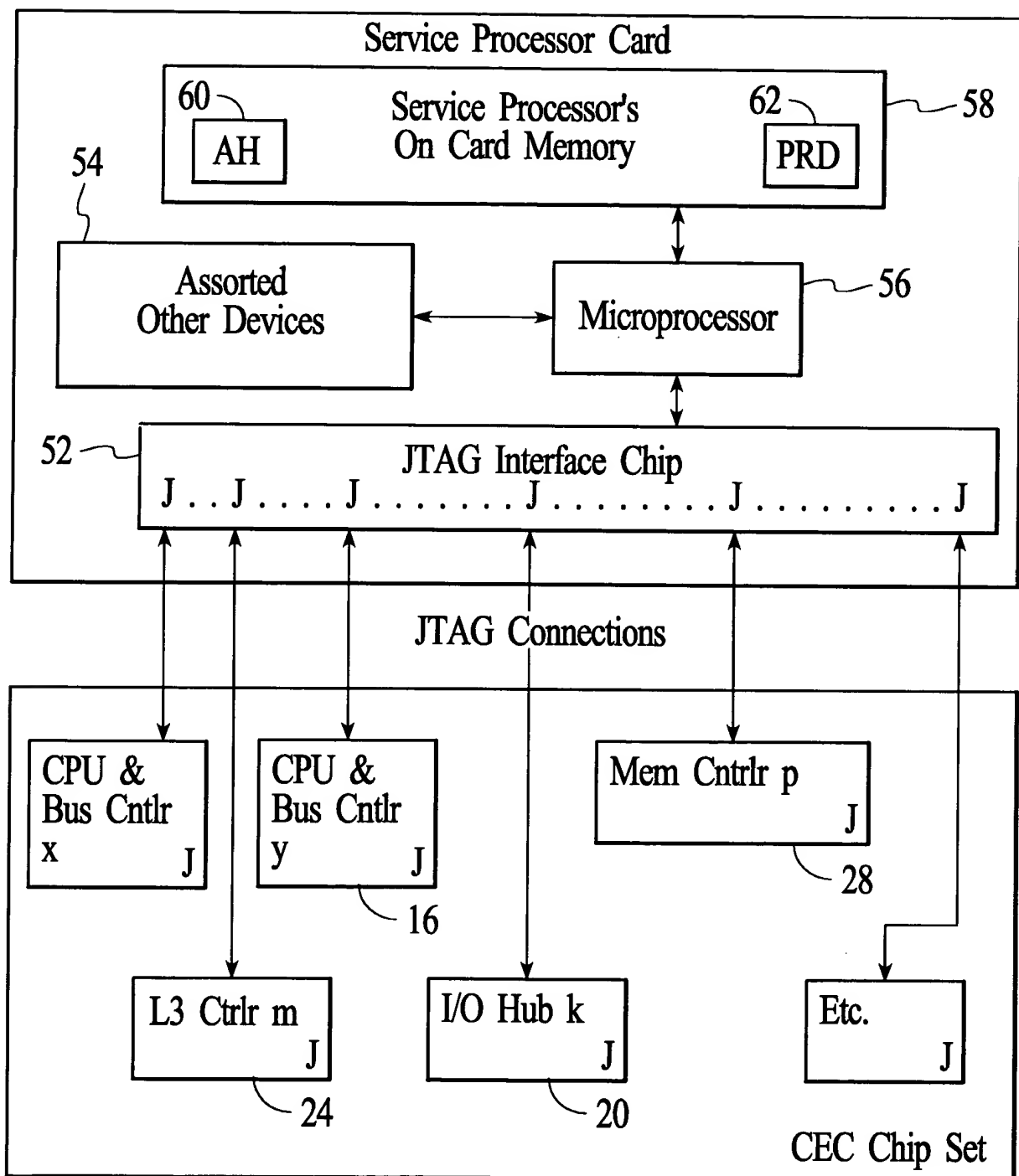
FIG. 1



J = chip has serial jtag connection to Service Processor HW

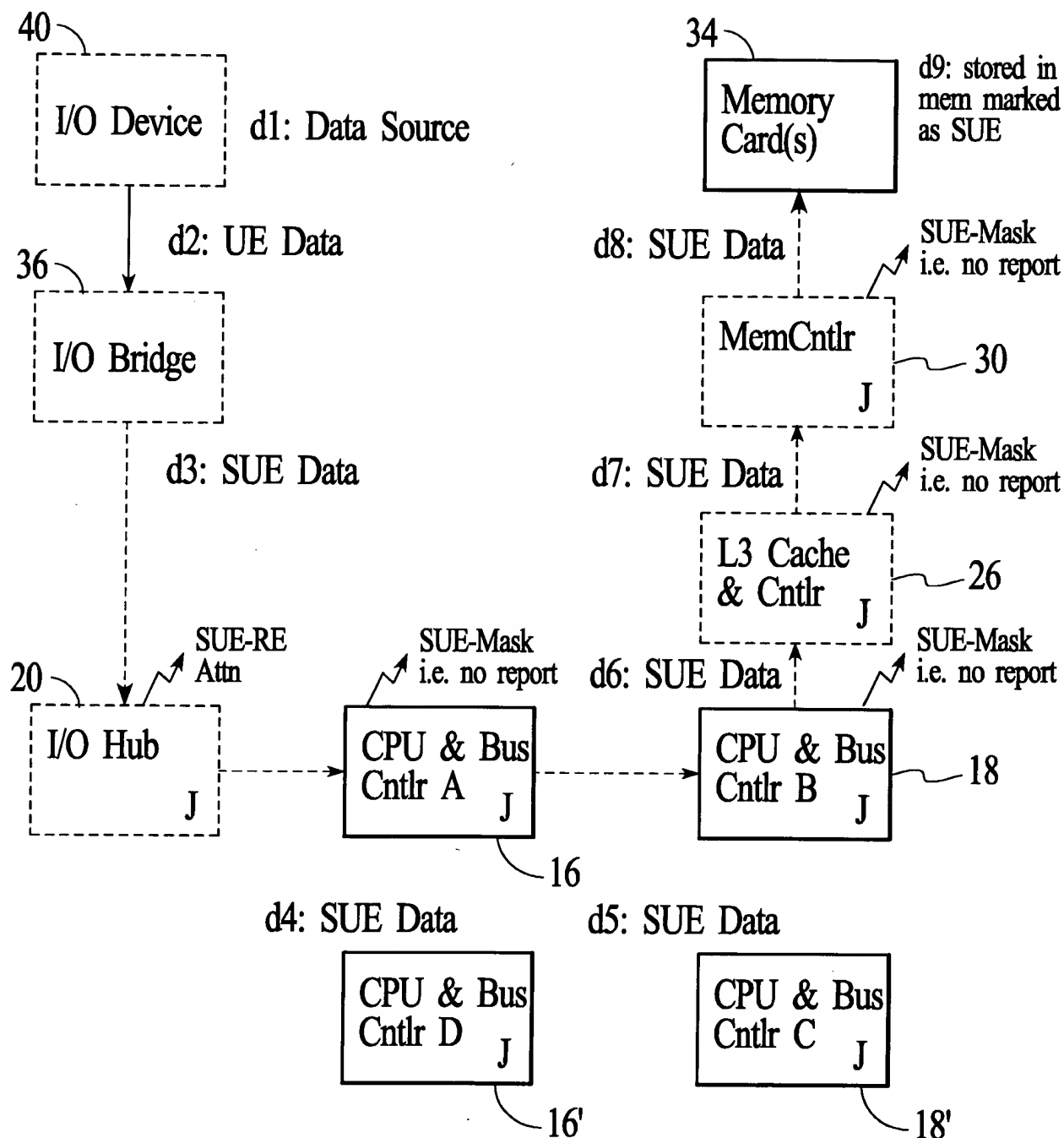
□ dotted box indicates remote chip or device inaccessible to SP via jtag

--- dotted line indicates connections to and between remote chips & devices



J = JTAG serial interface between Service Processor & chips in CEC

FIG. 3A

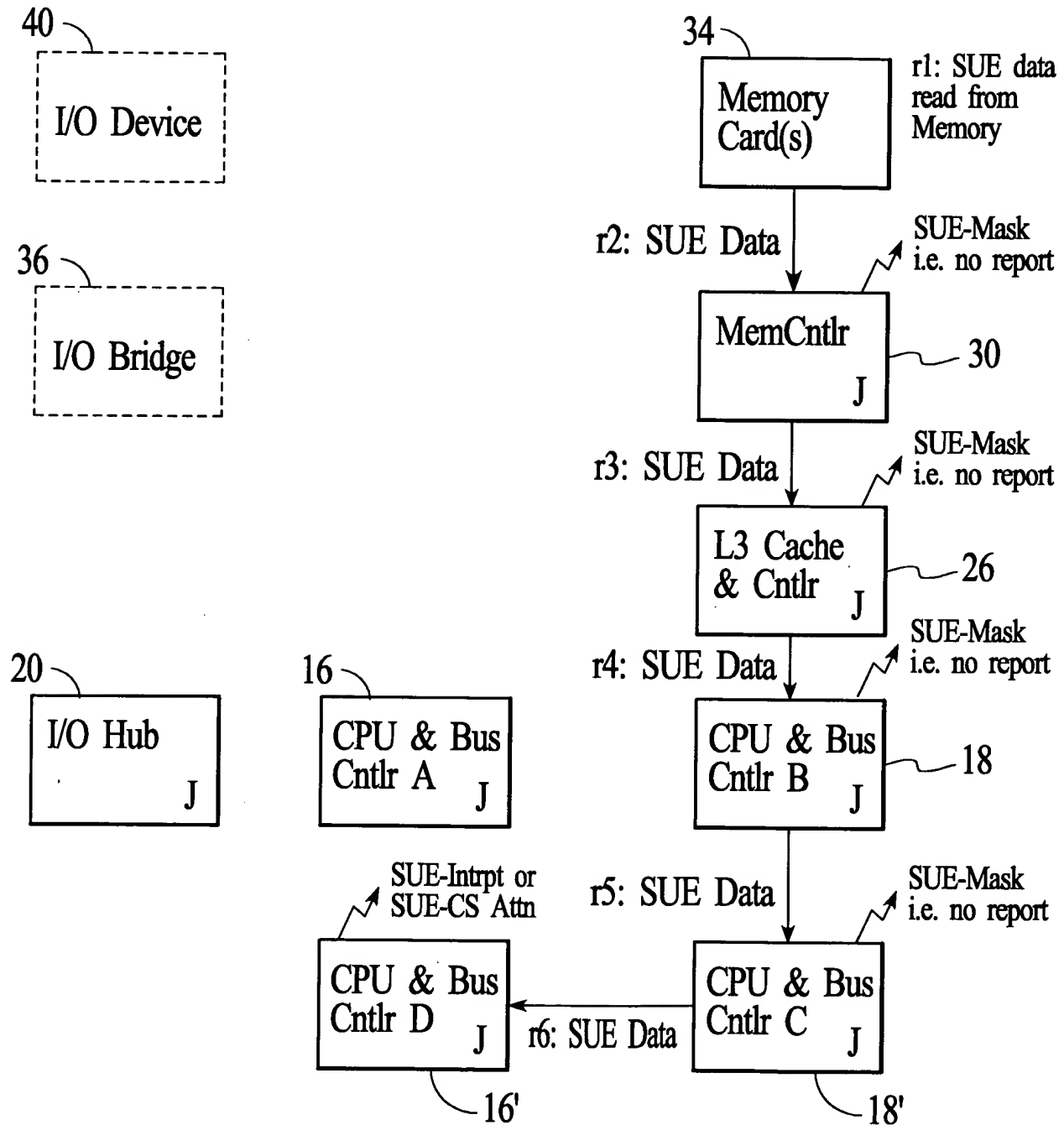


J = chip has serial jtag connection to Service Processor HW

□ dotted box indicates remote chip or device inaccessible to SP via jtag

---> dotted line indicates connections to and between remote chips & devices

FIG. 3B





09/820,458

5/8

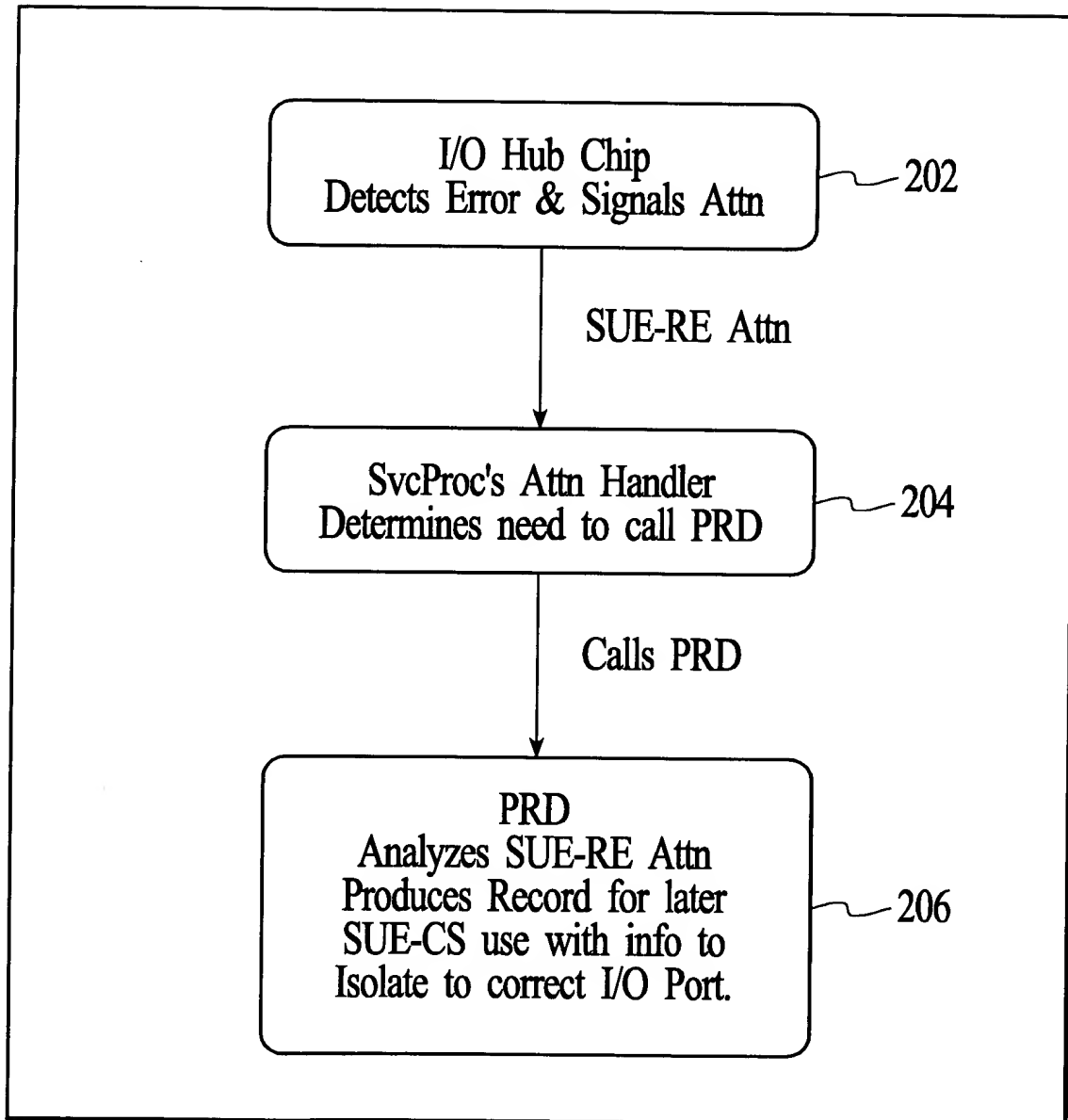


FIG. 4



09/820,758

6/8

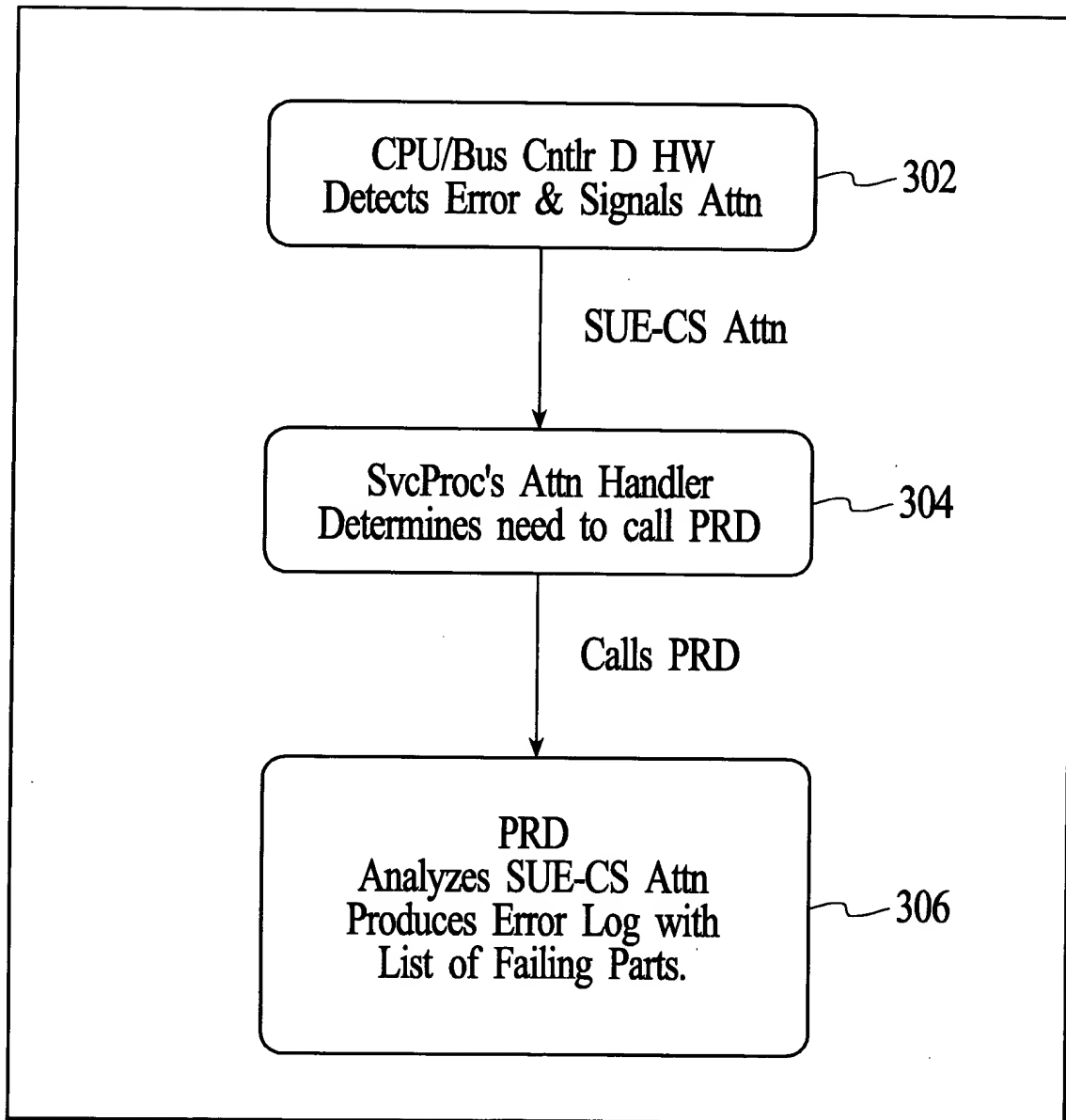
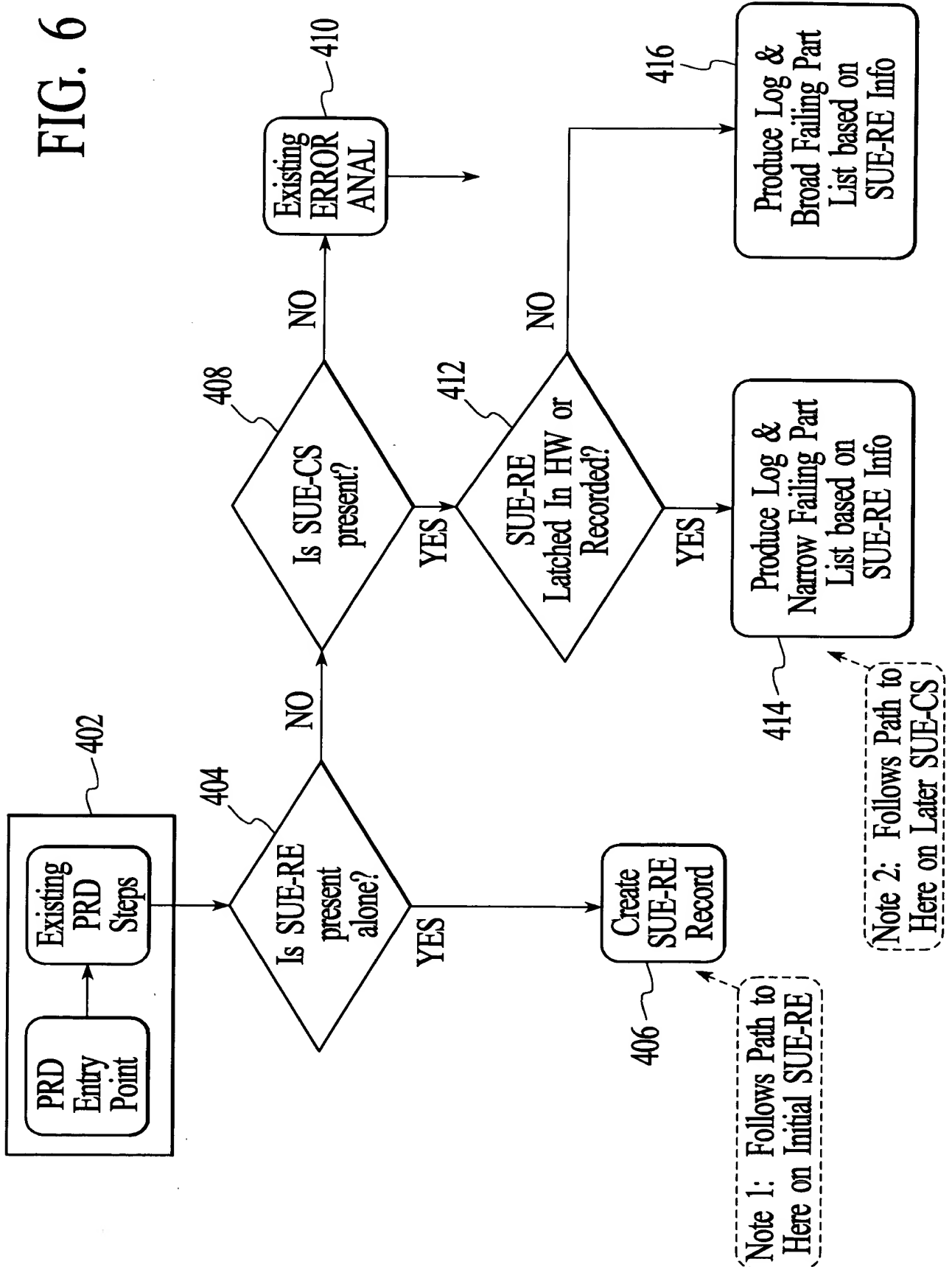


FIG. 5

FIG. 6





09/820,458

8/8

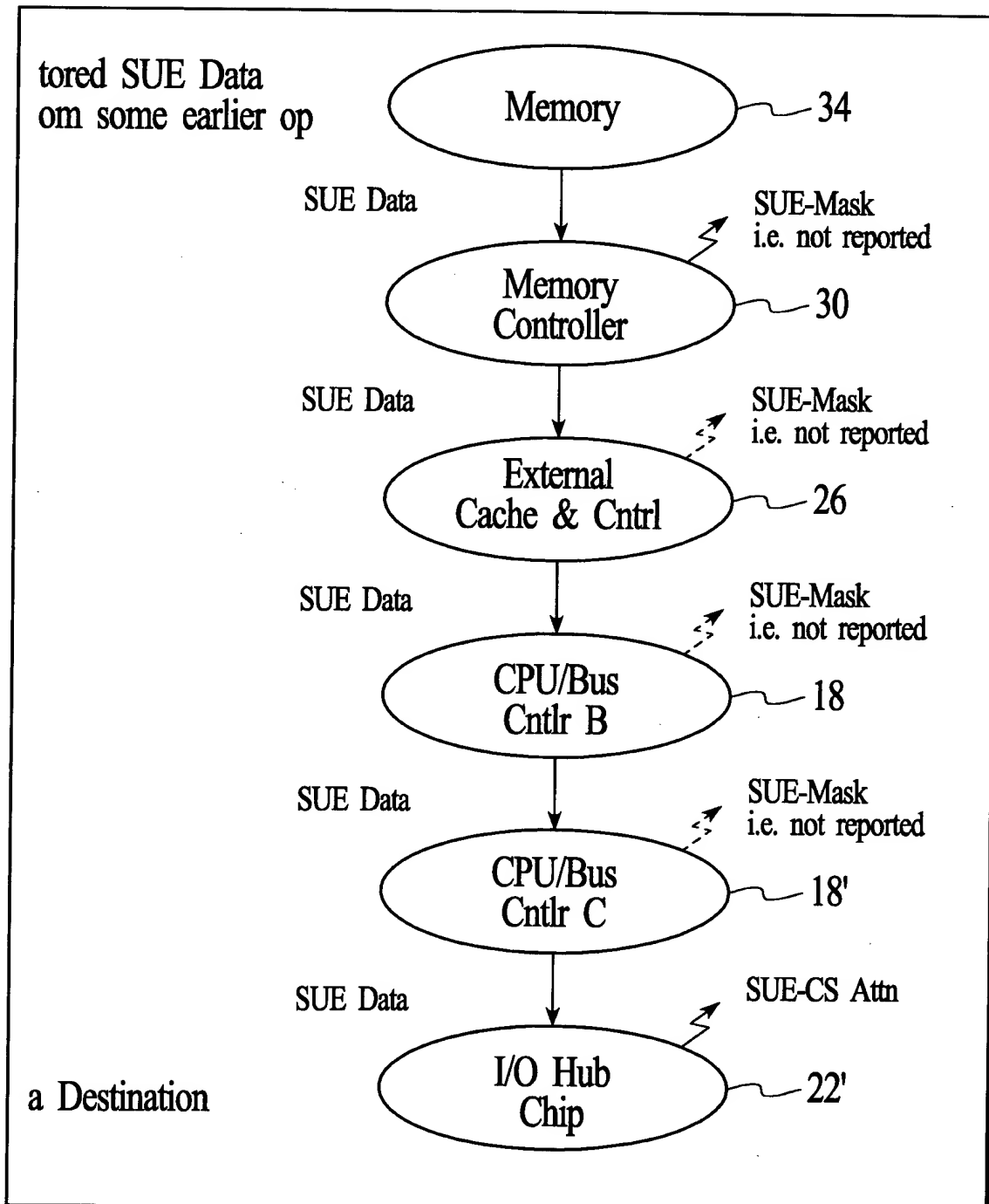


FIG. 7